CSF1 AMP or ATT Pin 1 Fig. 1 PRIOR ART ORC1 WCPL 1 CSF2 AMP or ATT Pin 2 ORC2 WCPL 2 Pin n WCPL n ORCn

Fig. 2 PRIOR ART

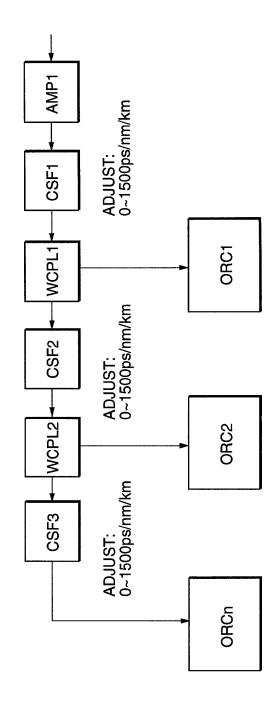


Fig. 3 PRIOR ART

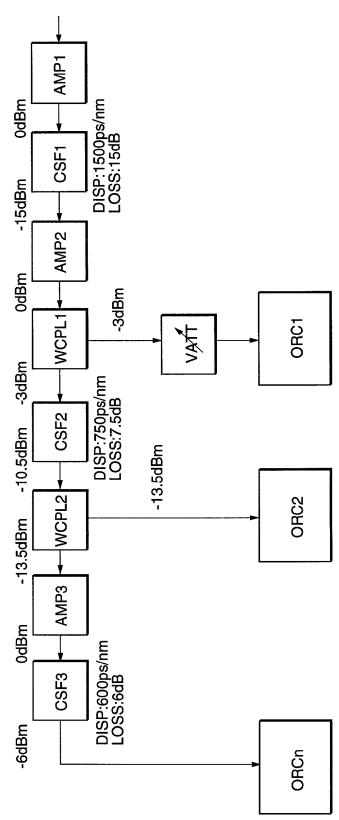
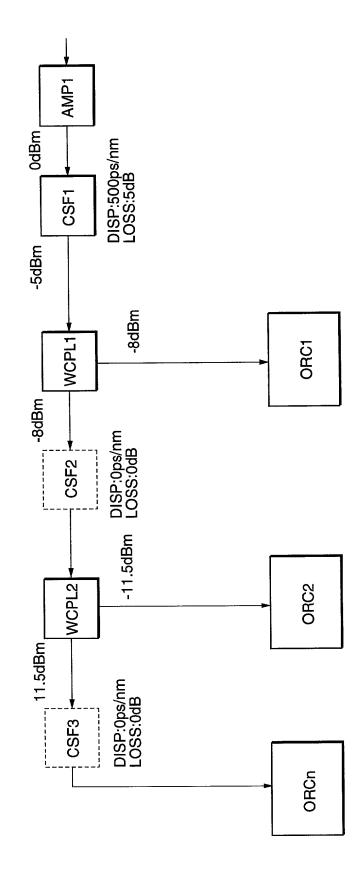


Fig. 4 PRIOR ART



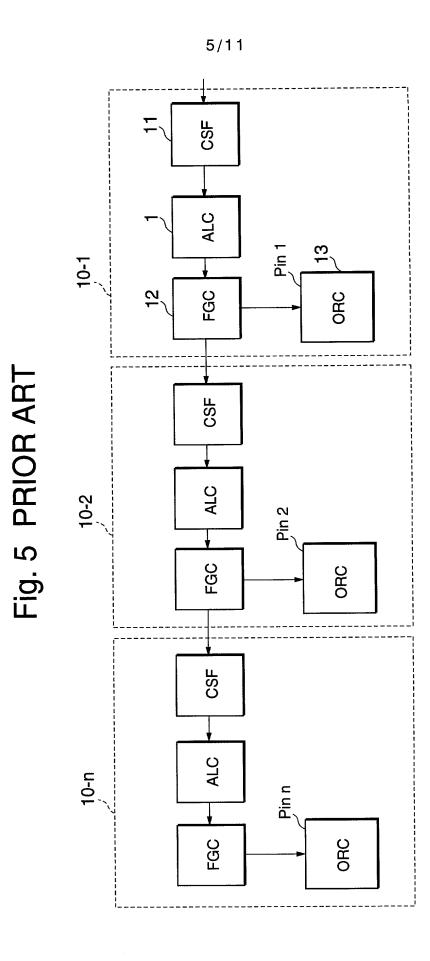


Fig. 6

